				- 1 A	ESSOR	Exami	inatio	m Sci	hedule	(Mark	3)	
Teaching Schedule Per Week			Progressive		Theory			Practical Ex.			Total	
Lectures	Practical	Credits	25 25		3 Hrs	3 Hrs 100		50			200	
. 3	2	5	- 25		Theory	Test	Tot	al	TW	PR	Gr To	tal
Pre-requisite		Source	Semester		75	25	10		25	50	175	
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ationale: N	Aicroprocessonstries and pr	ors 8085 for	n me basis l systems.	Therefo	re the kno	wiedge o	of mis	cropr	ocesso	ors is ve	ry essei	
arious indu	istries and pr	Cless contro	OURSE	ONTE	INTS							
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operation operat	& status si altiplexing. ions, Periph UCTION A and Instructi tion of instr n format – i	ND TIME on - Fetchi uction - da	NG ing, decod	ling and	l executio	m of ins	struc	tion.			10	20
Instructio Addressir Instructio	ng modes – i n timing an	Immediate, d operation	register, status,Ins	direct a	e tormat.	ct.				e state	12	25
Instructio Addressin Instructio 3. PROG Writing a opera techni transf	ng modes – n timing an <b>RAMMIN</b> ssembly lar tions, Logic iques – Loop er and 16 bi	Immediate, d operation G THE 808 guage prog operations ping, count t Arithmetic	register, status, Ins 5 grams for and Bran ing, index c Instruct	direct as struction data tra ch Open ting, Pro-	nd indire n cycle, n nsfer (co rations.St owding T ithmetic (	ct. nachine py) open udy of H ime dela Operatio	cycle ration Progr ays. ons re	e and ns.A: ramn Addi elate	l Time rithm ning tiona d to M	etic l data femory	-	25
Instructio Addressin Instructio 3. PROG Writing a operative technit transf 4. STAC Stack, Sr conce	ng modes – in timing an <b>RAMMIN</b> assembly lar tions, Logic iques – Looj er and 16 bi <b>K AND SU</b> abroutines, 6 pts.	Immediate, d operation G THE 808 oguage prog operations ping, counti t Arithmetic BROUTIN Conditional	register, status, Ins 5 grams for and Bran ing, index c Instruct	direct as struction data tra ch Open ting, Pro-	nd indire n cycle, n nsfer (co rations.St owding T ithmetic (	ct. nachine py) open udy of H ime dela Operatio	cycle ration Progr ays. ons re	e and ns.A: ramn Addi elate	l Time rithm ning tiona d to M	etic l data femory	-	
Instructio Addressin Instructio <b>3. PROG</b> Writing a operal techni transf <b>4. STAC</b> Stack, Si conce <b>5. 8085</b>	ng modes – n timing an <b>RAMMIN</b> issembly lar tions, Logic iques – Looj er and 16 bi <b>K AND SU</b> abroutines, ( ppts. <b>INTERRU</b> ion of the it	Immediate, d operation G THE 808 aguage prog operations ping, counti t Arithmetic BROUTIN Conditional PTS atterrupt pro	register, status,Ins 35 grams for and Bran ing, index c Instruct VES c call and cess. Mu	data tra ch Oper ing, Pro- ions. Ari return in	nd indire n cycle, n nsfer (co rations.St owding T thmetic ( nstruction terrupts (	ct. nachine py) oper udy of I ime dela Operatio ns, Adva & priorit	cycle ration Progr ays. ons re anceo	e and ns.Ai ramn Addi elated d sub	l Time rithm ning tiona d to M routiv	etic l data femory	8	1
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Familiarizing with full set of instructions of the given microprocessor 8085. Writing assembly & machine language program. The student is expected to perform a minimum of 8 exercises in the assembly language.

REFERENCE BOOKS: 1. Microprocessor Architecture, Programming & Applications by Ramesh S. Gaonkar 2. An Introduction to microprocessor by A.P. Mathur 3. Microprocessors, Theory & Applications by M. Rafiquzzaman

